Abstract

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A simulation system (30) for computer-implemented simulation and verification of a control system under development, the simulation system (30) comprising a plurality of simulation processes with corresponding memory and interface modules (2a-2k, 3a, 3b, 4, 6), which modules (2a-2k, 3a, 3b, 4, 6) comprise distinct memory locations for intermodule communication.

(Figure 2b)

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